MECHANISM FOR LOSSLESS, LOCK-FREE BUFFER SWITCHING IN AN ARBITRARY-CONTEXT TRACING FRAMEWORK

Abstract

A method for tracing on a processor including executing an executing control block on the processor to obtain data, wherein an interrupt on the processor is disabled prior to executing the execution control block and the interrupt is enabling after execution of the execution control block is completed, storing the data in a first buffer, wherein the first buffer is set to active, and setting the first buffer to inactive and setting a second buffer to active, wherein the interrupt on the processor is disabled prior to switching the first buffer to inactive and the interrupt is enabling after setting the second buffer to active.

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